

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of :

Yaron Kretchmer
Michael Porter
Thomas Obrien



Serial No.: 09/978,141

Group Art Unit: 2825

Filed : October 15, 2001

Examiner : Dimyan, Magid Y.

For : Automatic Method and System for Instantiating Built-In-Test (BIST) Modules in ASIC Memory Designs

Atty Docket : 2161P / 01-181

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Mark Salvatore

May 25, 2005

Date

Signature

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Official Draftsman

Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

Sir:

Applicant hereby substitutes the enclosed formal drawings for those presently in the above referenced application.

Respectfully submitted,

Sandeep Jaggi

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Date: 5/20/05



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Application Number	09/978,141
Filing Date	October 15, 2001
First Named Inventor	Kretchmer, Yaron
Art Unit	2825
Examiner Name	Dimyan, Magid Y.
Attorney Docket Number	01-181

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SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT		
Firm Name	LSI Logic Corporation	
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Printed name	Sandeep Jaggi	
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